

RESEARCH ARTICLE

First Principle Study of Tunnel Magnetoresistance of Various Oxide Materials

Sharif Saleem, Gul Faroz Ahmad Malik, Amir Farooq, Farooq Ahmad Khanday*

ABSTRACT: In this paper, the first principle study of tunnel magnetoresistance (TMR) for various oxide materials has been done. More than 30 materials have been taken into consideration and divided into four different categories based on their bandgap. The four categories are 2-4 eV bandgap materials, 4-6 eV bandgap materials, 6-8 eV bandgap materials, and 8-10 eV bandgap materials. The first principle simulations and the device-level simulations of various MTJ configurations were executed using the various oxide materials. The results reveal that maximum TMR ratio is observed in oxide materials having lower bandgap value and it reduces with the increase in oxide material bandgap. The maximum TMR ratio is observed in Cu₂O material and minimum TMR is observed in BeO material. The oxide materials with bandgap less than 3 eV are the best materials for device level fabrication and they provide 100% TMR for the oxide thickness up to 3 nm.

Keywords: Spintronics, Magnetoresistance, TMR, Magnetic Tunnel Junction.

Received: 23 March 2024; Revised: 27 April 2024; Accepted: 06 May 2024; Published Online: 11 July 2024

1. INTRODUCTION

Metal oxide semiconductor field effect transistor (MOSFET) is the building block of modern electronics and is frequently manufactured device [1]. The scaling and miniaturization of MOSFET increases the growth of the semiconductor technology drastically and enables highdensity integrated circuits (ICs) like memory chips and microprocessors [2]. MOSFET is a priary element of memory and the emergence of this device enables the use of metal oxide semiconductor (MOS) transistor as memory cell. John Schmidt developed the first MOS based memory in 1965. It was 64-bit metal oxide semiconductor static random access memory (MOS SRAM) [3]. This memory requires six transistors for each data bit. Later it was found that MOSFETs have ability to build capacitors that can store charge [4]. This property enables to write the charge either 1 or 0 and the MOS transistor could control writing the charge to capacitor. This leads the way to the development of dynamic random access

Author to whom correspondence should be addressed: farooqkhanday@kaghmiruniversity.ac.in (F. A. Khanday) memory (DRAM) cell. The prime advantages of MOS memory were high efficiency, cost effective, and less power requirement. These memories are widely used in analog storage, basic input/output system (BIOS) storage, cache memory, floating gate memory, and so on [5-9].

With the advancement in technology and when the technology node shrinks beyond 45 nm, power dissipation and performance becomes important concerns [1]. As the size of chips get reduced, the size of MOS capacitor also reduces. By scaling MOSFET, several limitations arise, such as subthreshold leakage current, time dependent dielectric breakdown, hot electron effects and several other short channel effects. Apart from these limitations, the main problems with the MOS technology are power dissipation and volatility.

There are several other technologies and alternate devices to reduce the power at circuit level. Some alternate devices are double gate MOSFET (DG-MOSFET), fin fieldeffect transistor (FIN FET), tunnel field-effect transistor (TFET), spin field-effect transistor (SPIN FET) and carbon nanotube field-effect transistor (CNT-FET) [2-5]. The alternate technologies are as straintronics, spintronics etc. [6- 7]. By all these methods several logics were developed [8], but all those logics suffer volatility problem.

To overcome these problems industries look for some better replacement. Spintronics is the technology, which has

Department of Electronics and Instrumentation Technology, University of Kashmir, Hazratbal, Srinagar-190006, Jammu and Kashmir, India.

capability to overcome both the issues of power dissipation and volatility. It is an emerging technology and have ability to enhance the future of electronics [9]. Apart from mass and charge, electrons have another basic property, i.e., spin. Spin is a pseudo vector quantity that has a predetermined magnitude of $\frac{h}{2}$ and has a variable polarization. h is reduced Plank's constant. However in past spin and charge have been treated separately. In classical electronics, information is stored in capacitors in the form of charge to save it and the charge of an electron is moved by electric field. In other standard technologies such as magnetic recording electron spin is used only through their macroscopic illustration. This technology exploits the spin property of an electron to encode information [10].

In charge based devices to switch from logic 0 to 1, the magnitude of charge is changed so current can flow from drain to source that makes it impossible to reduce the power dissipation as charge is a scaler quantity. On the other hand, in spintronic switching is attained by inverting the spin polarization without any change in the flow of current [11]. Some commonly used spintronic devices are spin field-effect transistor (spin FET), spin valve, spin MOSFET, spin tunnel field-effect transistor (spin TFET), magnetic tunnel junction (MTJ), spin transfer torque random access memory (STT-RAM), and so on $[12-16]$.

Magnetic tunnel junction (MTJ) is a spin based device which provides some important features like non- volatility, low power consumption and increased integration densities [17-30]. In past few years, researchers have shown the future of MTJs in many areas. Due to its non-volatility property, it is used as memory devices like Magnetic random access memories (MRAM) and Static Random Access Memories (SRAM) [18].

Scientists discovered an MTJ, Fe/Ge/Co, which provides the TMR ratio of about 14% at low temperature [19]. Ni/Nio/(Ni, Fe, Co) based MTJ was also reported in the literature, which demonstrated very low TMR at roomtemperature [20]. A MTJ with the TMR ratios $>10\%$ at roomtemperature was obtained in amorphous Al-oxide barrier and reported in the literature [21, 22] With the passage of time, TMR goes on increasing consistently year after year and reached up to 70% which is maximum TMR obtained using AlO barrier. After AlO barriers, the researchers move towards MgO barriers. The MgO barrier MTJ's provides TMR ratio >90% [23, 24]. In order to achieve high TMR ratio in MgO -based MTJ's, it requires epitaxial growth of the MgO layer and correct crystalline orientation on ferromagnetic electrodes, which is very difficult. There are several other MTJ's with different barrier materials like Fe/MgAl₂O₄/Fe [25]. Table 1 provides a comparative study of different MTJ devices reported in the literature with regards to magnetoresistance ratio.

The Fe/MgO/Fe MTJ configuration can provides 100% TMR ratio [25] but only at few atomic levels. This configuration provides 100% TMR ratio for oxide thickness 1nm to 1.2 nm. In this paper, it has been observed that materials with lower bandgap value provides 100% TMR ratio for oxide thickness 1 nm to 2 nm. E.g., Cu₂O etc.

The electrical control of magnetization in MTJs present significant progresses like speed, reliability, non-volatility, high efficiency of control and scalability. Tunnel Magnetoresistance TMR is an important parameter of MTJ. It is the electrical resistance of MTJ that depends on the layers and magnetic alignment of device [26-28]. For good quality readability and fast switching high TMR value is required. From the first experimental demonstration of TMR effect, [29] it was found that TMR value of MTJ also depends on the tunnel barrier quality.

Table 1. Comparative study of different MTJ devices reported in the literature with regard to magnetoresistance ratio.

MT.J Configurations	Tunnel magneto- resistance ratio	Ref.
$Co/Al2O3/Alq3/Co$	19%	$\lceil 26 \rceil$
Fe/MgO/Fe	100%	$\lceil 25 \rceil$
$Fe/Y_2O_3/Fe$	85%	$\lceil 25 \rceil$
Fe/Al ₂ O ₃ /Fe	90%	$\lceil 25 \rceil$
Fe/HfO ₂ /Fe	$<$ 100%	$\lceil 25 \rceil$
$CoFeB/Al-O/CoFeB$	80%	$\lceil 27 \rceil$
CoFeB/MgO/CoFeB	49.7%	$\lceil 28 \rceil$
Fe/MgO/FeCo(001)	60%	$\lceil 29 \rceil$
FeCo/MgO/FeCo	80%	$\lceil 30 \rceil$
This work	100%	

Fig. 1. Bar graph of Cu₂O and MgO.

With the emergence of big data and the unconventional computing, technologies require less time for writing data, storing them at a smaller scale with less energy consumption. This becomes the main driving force for MTJ research.

In this paper more than 30 materials have been analyzed and we found a strong relation between the bandgap of oxide material used and TMR of the MTJ. The materials were categorized on the basis of their bandgap and have been divided into four categories, 2-4 eV bandgap materials, 4-6 eV bandgap materials, 6-8 eV bandgap materials, and 8-10 eV bandgap materials. We perform the device-level simulations of these oxide materials using tool nanoHUB and obtain various results.

The results reveal that maximum TMR ratio is observed in oxide materials having lower bandgap value and it reduces with the increase in oxide material bandgap. It has been observed that oxide materials with lower bandgap value like Cu₂O, Tl₂O₃, CdO, HgO, PbO, Bi₂O₃, NiO are the best materials for device level fabrication and they provide 100% TMR for the oxide thickness up to 3nm.

2. MODELS AND METHODS

MTJ is an important spintronic device used for various logic and memory applications. Its working principle combines the magnetism and electronics and provides high speed, non-volatility and infinite tolerance. The basic principle of MTJ is tunneling magneto-resistance. In comparison with other non-volatile memories like NAND, Re-RAM and Pc-RAM , MTJ requires small write voltage 0.3V-0.4V, less write time 0.5-10 ns, less read time <5 ns and endurance up to 10^{15} .

MTJ is a three-layer device. It contains an insulator layer, which is sandwiched between two ferromagnetic materials. The insulator layer thickness is up to 2 nm and is made of materials like AlO, MgO, SiO2, ZnO, CdO, and high k materials etc. [31, 32]. The ferromagnetic layers thickness is up to 10nm and materials used for these layers are iron, nickel, cobalt, boron and Heusler alloys, and so on [33]. The lower ferromagnetic layer contains the electronics that have the same spin and is called as fixed layer or reference layer. The upper ferromagnetic layer contains electrons that change their spin in response to an external magnetic field or through a phenomenon called as spin transfer torque (STT). This layer

is called as free layer. When the spin of ferromagnetic layers is parallel the structure stores a 1 and when spin of ferromagnetic layers is opposite the structure stores a 0 [34, 35].

To read the data whether its 0 or 1, a small current is applied to MTJ structure. If the spins of the fixed layer and free layer are, parallel electrons can pass through the barrier, the resistance offered by the device for the flow of read current is less and it is denoted by parallel resistance R_P . When the spins are antiparallel, almost no electronics can tunnel through the barrier, the device offers more resistance to the flow of read current, and is denoted by antiparallel resistance R_{AP} . The resistance variation of the two states R_{P} and R_{AP} of the device is represented by the tunnel magnetoresistance (TMR).

$$
TMR = \frac{R_{AP} - R_P}{R_P}
$$

Depending upon the material, different MTJ's, exhibit different Spin-polarization and hence different spin dependent tunneling. In order to calculate the percentage change in junction resistance we measure TMR ratio by below given equation [36- 40].

$$
TMR \quad ratio = \frac{R_{AP} - R_P}{R_{AP}} \times 100
$$

By the recommendation of CA-PZ, Local density Approximation (LDA) has been used to evaluate the exchange-correlation energy [21]. The wave function is prolonged for plane-wave cutoff energy of about 300 eV for doped and undoped samples. In both of the samples, $5\times5\times6$ k-points has been applied to maintain the standards of convergence in calculating electronic characteristics and geometry optimization. Vanderbilt type ultrasoft pseudopotential has been described the electron-ion interaction. BFGS (Broyden-Fletcher-Goldfarb-Shanno) relaxation plan has been employed to optimize the dwelling.

Fig. 2. Basic structure of MTJ.

Fig. 3. Front view of MTJ switching from low resistance state to high resistance state.

The calculations are carried out on a unit cell and supercell. In addition, the molecular optimizations have been determined since the residual forces are below 0.05 eV. Geometry is optimized when total energy is 2×10^{-5} eV/atom, the pressure is the most (0.05 eV/Å), maximum stress is 0.1 GPa as well as the maximum atomic displacement is roughly 1.0×10^{-3} Å. All the calculations are actually run parallel to three occasions.

Fig. 4. (a) TMR vs oxide thickness of MTJ configurations having bandgap 2-3 eV. (b) TMR vs oxide thickness of MTJ configurations having bandgap 3-4 eV. (c) TMR vs oxide thickness of MTJ configurations having bandgap 4-6 eV. (d) TMR vs oxide thickness of MTJ configurations having bandgap 6-8 eV. (e) TMR vs oxide thickness of MTJ configurations having bandgap 8-11 eV.

and can be obtained at one or two atomic levels of oxide material.

3. RESULTS AND DISCUSSION

Fig. 4 (a-e) exhibit the TMR ratio Vs oxide thickness plots of the various MTJ configurations in parallel and anti-parallel magnetized states respectively. From these plots, it is clearly shown that with small bandgap oxide materials, the TMR ratio is very high up to 100%. These results clearly reveal that the oxide material with least bandgap value, below 5 nm provides the highest TMR ratio up to 100% and provide constant results for large oxide thickness up to 3 nm.

Fig. 5 shows the TMR plot of materials having bandgap 2- 3 eV. It is clearly seen that these materials provide the TMR ratio up to 100% and provide constant results for oxide thickness up to 3 nm. The bandgap of $Cu₂O$ oxide material is 2.04 that is least among all materials. This material provides best results among all materials as shown in figure 3.2 and can be used for device level fabrication. It is seen from the plot that $Fe/Cu₂O/Fe$ MTJ exhibits 100% TMR ratio constantly even when the insulator thickness is increased from 1nm to 2.6nm. The other MTJ configurations having oxide materials like Tl_2O_3 , CdO, HgO, PbO, Bi2O3, NiO also exhibits similar characteristics.

Fig. 5. TMR ratio vs oxide thickness of $Cu₂O$.

The second category of the materials that have been considered in this paper are the materials having bandgap 3-4 eV. The results of this category are also similar to the above materials but provides 100% TMR ratio for the insulator thickness less than 2 nm only. This category includes oxide materials like FeO, ZnO, In_2O_3 , SnO_2 , TiO_2 , Pr_2O_3 etc.

Fig. 6 shows TMR ratio vs oxide thickness of FeO. This material shows 100% TMR ratio for oxide thickness 1 nm - 2 nm. This material provides the best results in this category and can be used for device level fabrication of MTJ. The other categories of materials that are taken in this paper are the materials having bandgap 4-6 eV, materials having bandgap 6-8 eV and materials having bandgap 8-11 eV. These materials provide the similar characteristics as of first two categories but the only difference is that highest TMR is obtained only for few atomic levels of the oxide. The 100% TMR ratio is obtained only for oxide thickness of 1-1.4 nm.

The materials having bandgap above 6 eV, results are not same. In these materials, the highest TMR ratio obtained is 90%

Fig. 6. TMR ratio vs oxide thickness of FeO.

Fig. 7. TMR ratio vs oxide thickness of BeO and $Si₂O₂$.

Fig. 7 (a) and (b) shows the plot of TMR ratio vs oxide thickness of BeO and $Si₂O₂$. Both the materials provide highest TMR 100% only at 1nm and then decreases with the increase in the oxide thickness. At 1.8 nm, the TMR ratio is almost zero. From the above plot it is clearly seen that these materials are not best for MTJ device fabrication, because these materials provide 100% TMR only at 1nm oxide thickness.

4. CONCLUSION

In this paper, we investigated the TMR ratio of various MTJ configurations based on NEGF equations. The first principle study of TMR for various MTJ configurations has been carried out in this paper. We develop the quantum model of MTJ using the NEGF equations. By these simulations, we obtain the TMR verses thickness curves of each MTJ. In this paper, we calculate the TMR ratio of each MTJ and plot it against the thickness of oxide material used. By varying the thickness of oxide material, we observe the changes in TMR ratio. We compare the TMR curves of each MTJ model and obtain the best oxide material that provides the 100% TMR ratio. By these simulations, we also observe that the TMR ratio of an MTJ depends on the bandgap of the insulator material. We observe that the oxide material with the lower bandgap value provides best TMR ratio than the material with the higher bandgap value. We conclude that the oxide materials with lower bandgap value Tl_2O_3 , CdO, HgO, PbO, $Bi₂O₃$, NiO, FeO, ZnO, In₂O₃, SnO₂, TiO₂, Pr₂O₃ provides the TMR ratio up to 100 for thickness of oxide material from 1nm to 3nm. Also the oxide materials with the higher bandgap value La₂O₃, Er₂O₃, CaO, SrO, Al₂O₃, MgO, B₂O₃, Si₂O₃, BeO provides the 100% TMR ratio but for few atomic levels

of oxide material, 1nm to 1.4 nm only.

CONFLICT OF INTEREST

The authors declare that there is no conflict of interests.

REFERENCES

- [1] Maghroori, M. and Dolatshahi, M., 2021. Design of lowpower CMOS VLSI circuits using multi-objective optimization in genetic algorithms. *World Journal of Advanced Research and Reviews*, *12*(1), pp.215-224.
- [2] Iwai, H., 2009. Roadmap for 22 nm and beyond. *Microelectronic Engineering*, *86*(7-9), pp.1520- 1528.
- [3] Dasgupta, A., Agarwal, A. and Chauhan, Y.S., 2017. Unified compact model for nanowire transistors including quantum effects and quasi-ballistic transport. *IEEE Transactions on Electron Devices*, *64*(4), pp.1837-1845.
- [4] Goharrizi, A.Y., Pourfath, M., Fathipour, M. and Kosina, H., 2012. Device performance of graphene nanoribbon field-effect transistors in the presence of line-edge roughness. *IEEE Transactions on electron devices*, *59*(12), pp.3527-3532.
- [5] Rezali, F.A.M., Othman, N.A.F., Mazhar, M., Hatta, S.W.M. and Soin, N., 2016. Performance and device design based on geometry and process considerations for 14/16-nm strained FinFETs. *IEEE Transactions on Electron Devices*, *63*(3), pp.974-981.
- [6] Flatte, M.E., 2007. Spintronics. *IEEE transactions on electron devices*, *54*(5), pp.907-920.
- [7] Malik, G.F.A., Kharadi, M.A., Parveen, N. and Khanday, F.A., 2020. Modelling for triple gate spin‐FET and design of triple gate spin‐FET‐based binary adder. *IET Circuits, Devices & Systems*, *14*(4), pp.464-470.
- [8] Moaiyeri, M.H., Nasiri, M. and Khastoo, N., 2016. An efficient ternary serial adder based on carbon nanotube FETs. *Engineering Science and Technology, an International Journal*, *19*(1), pp.271-278.
- [9] Datta, S. and Das, B., 1990. Electronic analog of the electro‐optic modulator. *Applied Physics Letters*, *56*(7), pp.665-667.
- [10]Maciel, N., Marques, E., Naviner, L., Zhou, Y. and Cai, H., 2019. Magnetic tunnel junction applications. *Sensors*, *20*(1), p.121.
- [11]Joshi, V.K., Barla, P., Bhat, S. and Kaushik, B.K., 2020. From MTJ device to hybrid CMOS/MTJ circuits: A review. *IEEE Access*, *8*, pp.194105-194146.
- [12]Kharadi, M.A., Malik, G.F.A., Khanday, F.A. and Shah, K.A., 2020. Hydrogenated silicene based magnetic junction with improved tunneling magnetoresistance and spin-filtering efficiency. *Physics Letters A*, *384*(32), p.126826.
- [13]Tahara, T., Koike, H., Kameno, M., Sasaki, T., Ando, Y., Tanaka, K., Miwa, S., Suzuki, Y. and Shiraishi, M., 2015. Room-temperature operation of Si spin MOSFET with high on/off spin signal ratio. *Applied Physics Express*, *8*(11), p.113004.
- [14]Diény, B., 1994. Giant magnetoresistance in spin-valve multilayers. *Journal of Magnetism and Magnetic Materials*, *136*(3), pp.335-359.
- [15]Wang, G., Wang, Z., Klein, J.O. and Zhao, W., 2017. Modeling for spin-FET and design of spin-FET-based logic gates. *IEEE Transactions on Magnetics*, *53*(11), pp.1-6.
- [16] Turkane, S.M. and Kureshi, A.K., 2016. Review of tunnel field effect transistor (TFET). *International*

Journal of Applied Engineering Research, *11*(7), pp.4922-4929.

- [17] Sharma, B., Kundu, C., Rahaman, H. and Sarkar, C.K., 2018. Study of TMR with Different Ferromagnetic Material and Variations in Spin-Split, Thickness and Oxide Barrier Height of a MTJ Memory Device. *Journal of Nanoelectronics and Optoelectronics*, *13*(2), pp.245- 250.
- [18] Parkin, S.S., Kaiser, C., Panchula, A., Rice, P.M., Hughes, B., Samant, M. and Yang, S.H., 2004. Giant tunnelling magnetoresistance at room temperature with MgO (100) tunnel barriers. *Nature materials*, *3*(12), pp.862-867.
- [19] Maekawa, S. and Gafvert, U., 1982. Electron tunneling between ferromagnetic films. *IEEE Transactions on Magnetics*, *18*(2), pp.707-708.
- [20] Moodera, J.S., Nassar, J. and Mathon, G., 1999. Spintunneling in ferromagnetic junctions. *Annual Review of Materials Science*, *29*(1), pp.381-432.
- [21]Miyazaki, T. and Tezuka, N., 1995. Spin polarized tunneling in ferromagnet/insulator/ferromagnet junctions. *Journal of Magnetism and Magnetic Materials*, *151*(3), pp.403-410.
- [22]Butler, W.H., Zhang, X.G., Schulthess, T.C. and MacLaren, J.M., 2001. Spin-dependent tunneling conductance of Fe| MgO| Fe sandwiches. *Physical Review B*, *63*(5), p.054416.
- [23] Mathon, J. and Umerski, A., 2001. Theory of tunneling magnetoresistance of an epitaxial Fe/MgO/Fe (001) junction. *Physical Review B*, *63*(22), p.220403.
- [24]Miura, Y., Muramoto, S., Abe, K. and Shirai, M., 2012. First-principles study of tunneling magnetoresistance in Fe/MgAl₂O₄/Fe(001) magnetic tunnel junctions. *Physical Review B*, *86*(2), p.024426.
- [25] Dey, D., De, D., Ahmadian, A., Ghaemi, F. and Senu, N., 2021. Electrically doped nanoscale devices using firstprinciple approach: a comprehensive survey. *Nanoscale research letters*, *16*, pp.1-16.
- [26] Gani, M., Shah, K.A., Parah, S.A. and Misra, P., 2020. Room temperature high giant magnetoresistance graphene based spin valve and its application for realization of logic gates. *Physics Letters A*, *384*(7), p.126171.
- [27]Hirohata, A., Yamada, K., Nakatani, Y., Prejbeanu, I.L., Diény, B., Pirro, P. and Hillebrands, B., 2020. Review on spintronics: Principles and device applications. *Journal of Magnetism and Magnetic Materials*, *509*, p.166711.
- [28] Parkin, S., Jiang, X., Kaiser, C., Panchula, A., Roche, K. and Samant, M., 2003. Magnetically engineered

spintronic sensors and memory. *Proceedings of the IEEE*, *91*(5), pp.661-680.

- [29]Bowen, M., Cros, V., Petroff, F., Fert, A., Martınez Boubeta, C., Costa-Krämer, J.L., Anguita, J.V., Cebollada, A., Briones, F., De Teresa, J.M. and Morellón, L., 2001. Large magnetoresistance in Fe/MgO/FeCo (001) epitaxial tunnel junctions on GaAs (001). *Applied Physics Letters*, *79*(11), pp.1655-1657.
- [30]Kubota, H., Fukushima, A., Yakushiji, K., Nagahama, T., Yuasa, S., Ando, K., Maehara, H., Nagamine, Y., Tsunekawa, K., Djayaprawira, D.D. and Watanabe, N., 2008. Quantitative measurement of voltage dependence of spin-transfer torque in MgO-based magnetic tunnel junctions. *Nature Physics*, *4*(1), pp.37-41.
- [31] Moodera, J.S., Kinder, L.R., Wong, T.M. and Meservey, R., 1995. Large magnetoresistance at room temperature in ferromagnetic thin film tunnel junctions. *Physical review letters*, *74*(16), p.3273.
- [32]Yuasa, S., Nagahama, T., Fukushima, A., Suzuki, Y. and Ando, K., 2004. Giant room-temperature magnetoresistance in single-crystal Fe/MgO/Fe magnetic tunnel junctions. *Nature materials*, *3*(12), pp.868-871.
- [33]Wuttig, M., 2005. Towards a universal memory?. *Nature Materials*, *4*(4), pp.265-266.
- [34]Liu, Q., Gao, S., Xu, L., Yue, W., Zhang, C., Kan, H., Li, Y. and Shen, G., 2022. Nanostructured perovskites for

nonvolatile memory devices. *Chemical Society Reviews*, *51*(9), pp.3341-3379.

- [35] Julliere, M., 1975. Tunneling between ferromagnetic films. *Physics Letters A*, *54*(3), pp.225-226.
- [36] Sinha, R. and Kaur, J., 2022. Analyzing the impact of different composite dielectrics on performance parameters of a magnetic tunnel junction memory device. *Journal of Electronic Materials*, *51*(10), pp.5686-5703.
- [37]Datta, D., Behin-Aein, B., Datta, S. and Salahuddin, S., 2011. Voltage asymmetry of spin-transfer torques. *IEEE Transactions on Nanotechnology*, *11*(2), pp.261-272.
- [38] Sinha, R. and Kaur, J., 2022. Analyzing the impact of different composite dielectrics on performance parameters of a magnetic tunnel junction memory device. *Journal of Electronic Materials*, *51*(10), pp.5686-5703.
- [39]Munira, K., Butler, W.H. and Ghosh, A.W., 2012. A quasi-analytical model for energy-delay-reliability tradeoff studies during write operations in a perpendicular STT-RAM cell. *IEEE transactions on electron devices*, *59*(8), pp.2221-2226.
- [40]Uribe, M.D.R., Magana, A.J., Bahk, J.H. and Shakouri, A., 2016. Computational simulations as virtual laboratories for online engineering education: A case study in the field of thermoelectricity. *Computer Applications in Engineering Education*, *24*(3), pp. 428- 442.